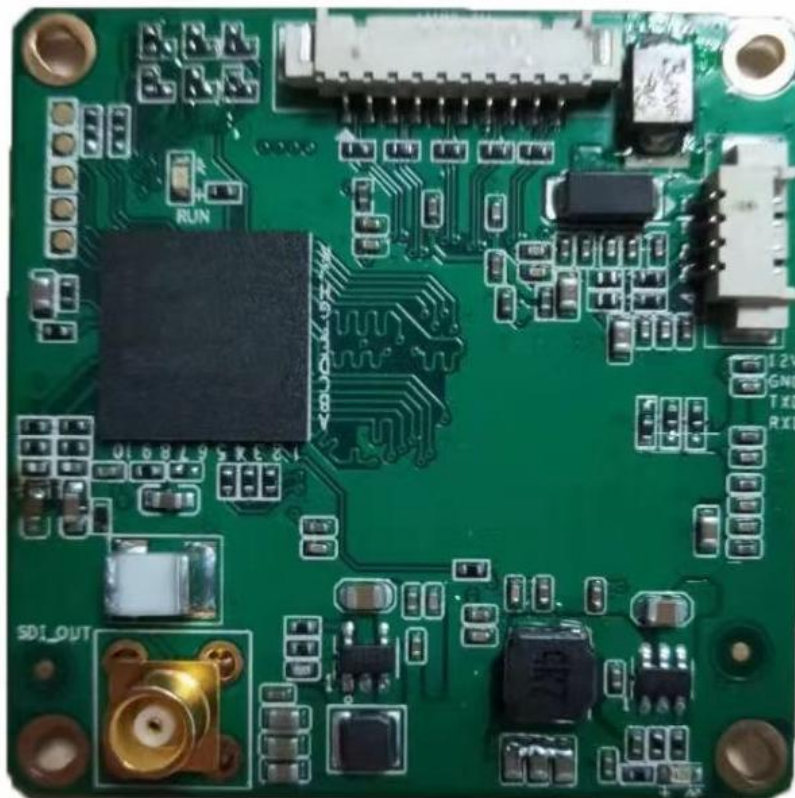
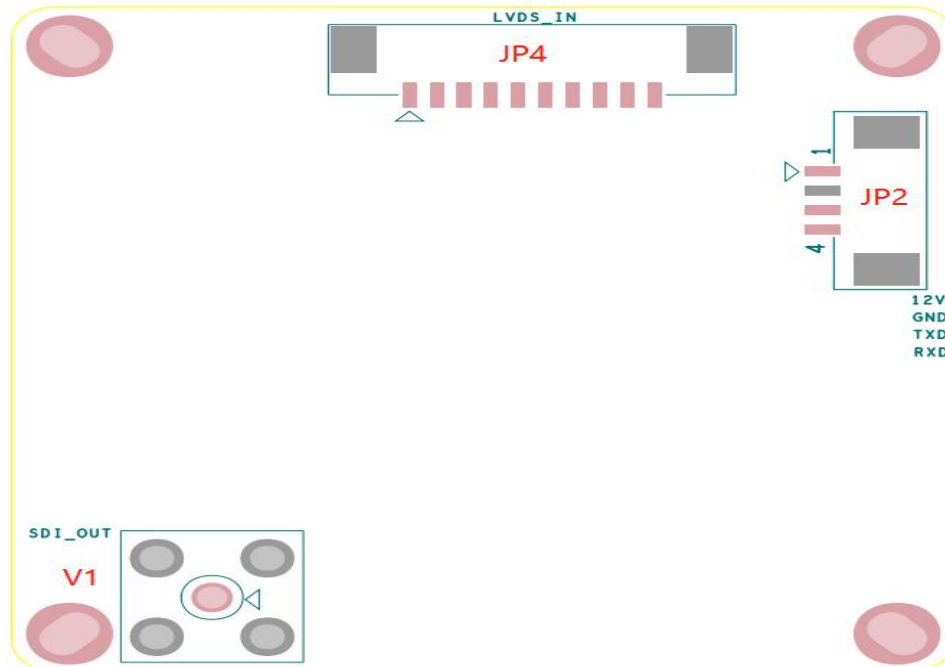
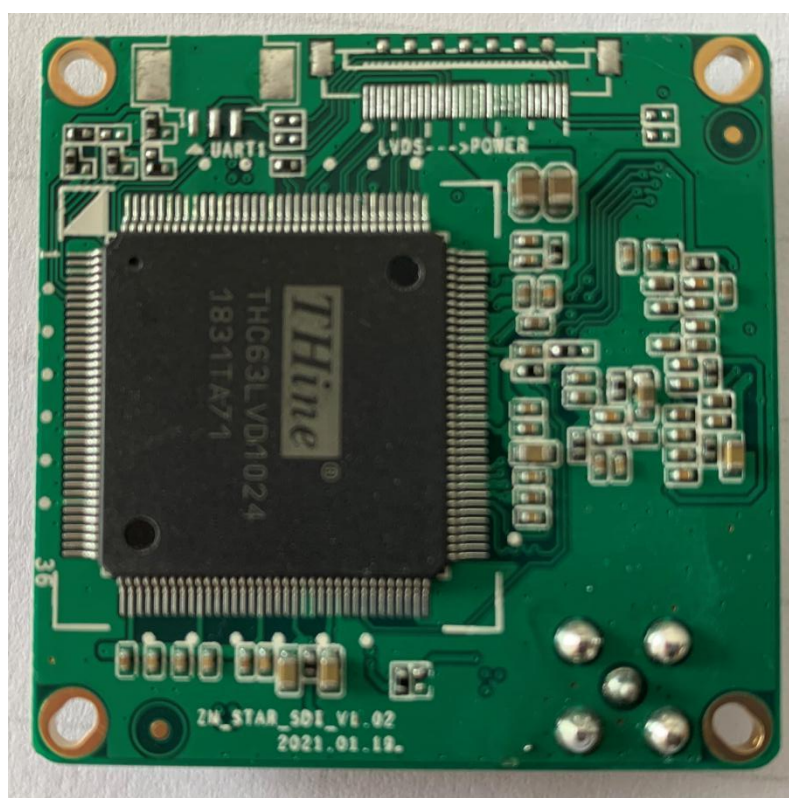
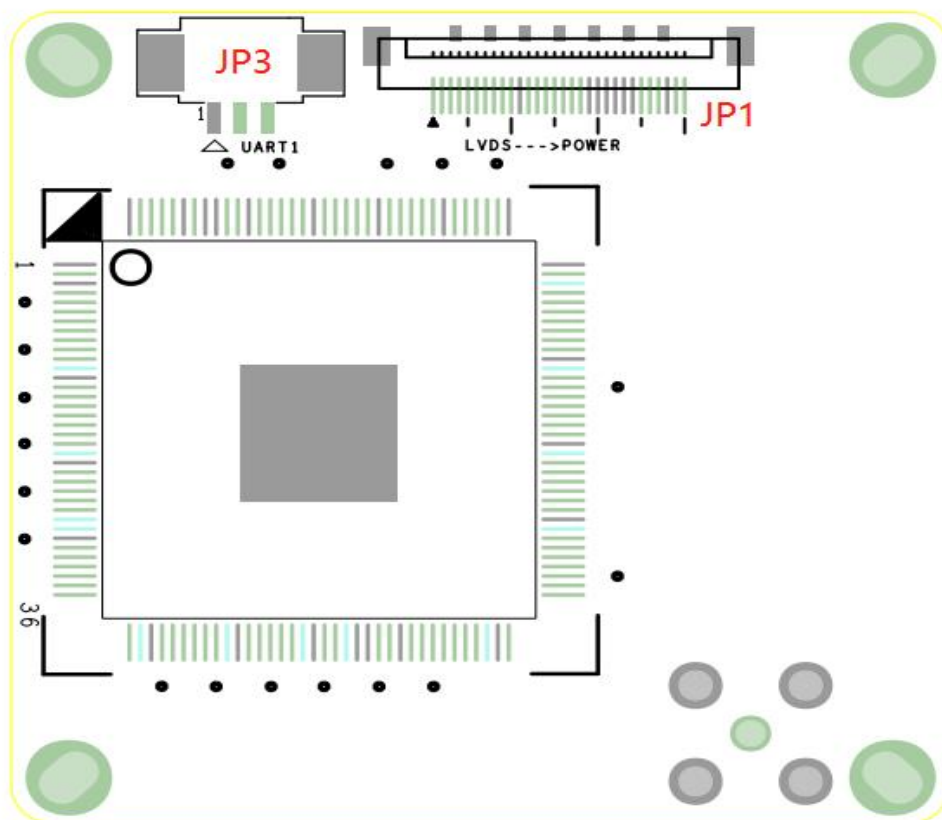


SDI Interface Board



Front



Back

Type	Pin	Pin Name	Description
V1: SDI Interface			SDI signal output
JP1: 30pin FPC Connector LVDS	1	LVDS_TD+	LVDS4 data P
	2	LVDS_TD-	LVDS4 data N
	3	LVDS_TCLK+	LVDS clock data P
	4	LVDS_TCLK-	LVDS clock data N
	5	LVDS_TC+	LVDS3 data P
	6	LVDS_TC-	LVDS3 data N
	7	LVDS_TB+	LVDS2 data P
	8	LVDS_TB-	LVDS2 data N
	9	LVDS_TA+	LVDS1 data P
	10	LVDS_TA-	LVDS1 data N
	11	GND	Power ground
	12	UART3_TXD	UARTport, camera module transmit signal (3.3V), Visca protocol
	13	UART3_RXD	UARTport, camera module receive signal (3.3V), Visca protocol
	14	DC_12V	DC12V power
	15	DC_12V	DC12V power
	16	DC_12V	DC12V power
	17	DC_12V	DC12V power
	18	DC_12V	DC12V power
	19	GND	Power ground
	20	GND	Power ground
	21	GND	Power ground
	22	GND	Power ground
	23	GND	Power ground
	24	GND	Power ground
	25	NC	/
	26	NC	/
	27	NC	/
	28	GND	Power ground
	29	UART1_TXD	UART port, camera module transmit signal (3.3V), reserved
	30	UART1_RXD	UART port, camera module receive signal (3.3V), reserved
JP2: 4pin UART Port (1.25mm connector)	1	DC12V	DC12V power
	2	GND	Power ground
	3	UART3_TXD	UART port, camera module transmit signal (3.3V), Visca protocol
	4	UART3_RXD	UART port, camera module receive signal (3.3V), Visca protocol
JP3: 3pinRS232 (1.25mm connector)	1	GND	Power ground
	2	UART1_RXD	UART 口, 机芯端接收信号 (3.3V) , 预留
	3	UART1_TXD	UART 口, 机芯端发送信号 (3.3V) , 预留
	1	LVDS_TA-	LVDS1 data N
	2	LVDS_TA+	LVDS1 data P

JP4: 10pinLVDS (1.25mm connector)	3	LVDS_TB-	LVDS2 data N
	4	LVDS_TB+	LVDS2 data P
	5	LVDS_TC-	LVDS3 data N
	6	LVDS_TC+	LVDS3 data P
	7	LVDS_TCLK-	LVDS clock data N
	8	LVDS_TCLK+	LVDS clock data P
	9	LVDS_TD-	LVDS4 data N
	10	LVDS_TD+	LVDS4 data P